



MAS-FIN-419

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By: Wangzhong Chen

Date: December 30, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/723,906 Confirmation No. 1149
Applicant : Wolfgang Hetzel et al.
Filed : November 26, 2003
TC/A.U. : 2811
Examiner : Hung K. Vu
Title : Electronic Component Having Stacked
Semiconductor Chips in Parallel, and a Method
for Producing the Component

Docket No. : MAS-FIN-419

Customer No. : 24131

Hon. Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

AMENDMENT

S i r :

In response to the Office action dated October 20, 2004,
please amend the above-identified application as follows:

Amendments to the Claims are reflected in the listing of
claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 11 of this paper.